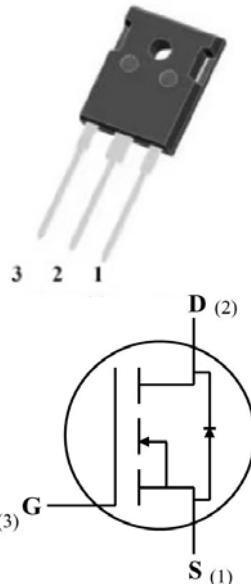


## Silicon Carbide Power MOSFET (N-Channel Enhancement)

$V_{DS}$	1200V
$I_D(25^\circ C)$	46A
$R_{DS(on)}$	50mΩ



### Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

### Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

### Mechanical Data

- **Package:** TO-247AB
- **Terminals:** Tin plated leads
- **Polarity:** As marked

### ■Maximum Ratings ( $T_c=25^\circ C$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code				D212060NCTYG3	
Drain source voltage @ $T_j=25^\circ C$	$V_{DS,max}$	V	1200	$V_{GS}=0V, I_D=100\mu A$	
Gate source voltage @ $T_j=25^\circ C$	$V_{GS,max}$	V	-8/+22	Absolute maximum values (AC f >1Hz, duty cycle < 1%)	
Gate source voltage @ $T_j=25^\circ C$	$V_{GS,op}$	V	-5/+18	Recommended operational values	
Continuous drain current @ $T_c=25^\circ C$	$I_D$	A	46	$V_{GS}=18V, T_c=25^\circ C$	Fig.17
Continuous drain current @ $T_c=100^\circ C$			31	$V_{GS}=18V, T_c=100^\circ C$	
Pulsed drain current	$I_{D,pulsed}$	A	90	Pulse width $t_p$ limited by $T_{j,max}$	Fig.22
Avalanche energy,Single pulse	$E_{AS}$	mJ	534	$V_{DD}=75V, L=30mH$	
Power Dissipation	$P_{TOT}$	W	214	$T_c=25^\circ C, T_j = 175^\circ C$	Fig.16
Power Dissipation			107	$T_c=100^\circ C, T_j = 175^\circ C$	
Operating junction and Storage temperature range	$T_j, T_{stg}$	°C	-55 to +175		
Soldering temperature	$T_L$	°C	260	1.6mm (0.063") from case for 10s	
Mounting torque	$T_M$	Nm	0.6	M3 screw Maximum of mounting process: 3	



# YJD212060NCTYG3

## ■ Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	V <sub>GS(th)</sub>	V	2.0	3.0	4.0	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =8mA	Fig.4, 11
				2.3		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =8mA, Tj=175°C	
Drain source breakdown voltage	V <sub>(BR)DSS</sub>	V	1200			V <sub>GS</sub> =0V, I <sub>D</sub> =100uA	
Drain source leakage current	I <sub>DSS</sub>	uA		10	100	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V	
Gate source leakage current	I <sub>GSS</sub>	nA		10	100	V <sub>GS</sub> =18V, V <sub>DS</sub> =0V	
Current drain source on-state resistance	R <sub>DS ON</sub>	mΩ		60		V <sub>GS</sub> =15V, I <sub>D</sub> =20A	Fig.5, 6, 7
				50	66	V <sub>GS</sub> =18V, I <sub>D</sub> =20A	
				100		V <sub>GS</sub> =15V, I <sub>D</sub> =20A, Tj=175°C	
				92		V <sub>GS</sub> =18V, I <sub>D</sub> =20A, Tj=175°C	
Transconductance	g <sub>f</sub>	S		15		V <sub>DS</sub> =20V, I <sub>D</sub> =20A	Fig.4
				14.5		V <sub>DS</sub> =20V, I <sub>D</sub> =20A, Tj=175°C	

## ■ Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C <sub>iss</sub>	pF		2138		V <sub>DS</sub> =800V, V <sub>GS</sub> =0V, Tj=25°C, f=1 MHz, V <sub>AC</sub> = 25mV	Fig.13, 14
Output capacitance	C <sub>oss</sub>			88			
Reverse capacitance	C <sub>rss</sub>			4.6			
C <sub>oss</sub> stored energy	E <sub>oss</sub>	uJ		35			Fig.15
Gate source charge	Q <sub>gs</sub>	nC		21.4		V <sub>DS</sub> =800V, V <sub>GS</sub> =-5/+18V, I <sub>D</sub> =20A	Fig.12
Gate drain charge	Q <sub>gd</sub>			41.6			
Gate charge	Q <sub>g</sub>			78.8			
Internal gate resistance	R <sub>g</sub>	Ω		1.5		f=1MHz	

## ■ Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	t <sub>d(on)</sub>	ns		10.8		V <sub>DD</sub> =800V, V <sub>GS</sub> =-5/+18V, I <sub>D</sub> =20A, R <sub>g</sub> =2.4Ω, L=100uH	Fig.21
Rise time	t <sub>r</sub>			12.4			
Turn off delay time	t <sub>d(off)</sub>	ns		20.8			
Fall time	t <sub>f</sub>			16.3			
Turn on switching energy	E <sub>on</sub>	uJ		218		V <sub>DD</sub> =800V, V <sub>GS</sub> =-5/+18V, I <sub>D</sub> =20A, R <sub>g</sub> =2.4Ω, L=100uH	Fig.19, 20
Turn off switching energy	E <sub>off</sub>			59			



**■Body diode characteristics (Tc=25°C unless otherwise specified )**

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V <sub>SD</sub>	V		4.3		V <sub>GS</sub> =-5V, I <sub>SD</sub> =10A	Fig.8
				3.1		V <sub>GS</sub> =0V, I <sub>SD</sub> =10A, T <sub>j</sub> =175°C	Fig.9
Continuous diode forward current	I <sub>S</sub>	A		39		V <sub>GS</sub> =-5V, T <sub>c</sub> =25°C	
Reverse recovery time	trr	nS		15.6		V <sub>R</sub> =800V, V <sub>GS</sub> =-5V, I <sub>D</sub> =20A, di/dt=2000A/uS	
Reverse recovery charge	Qrr	nC		127			
Peak reverse recovery current	Irrm	A		13.6			

**■Thermal Characteristics (Ta=25°C Unless otherwise specified)**

PARAMETER	SYMBOL	UNIT	Typ.
Thermal resistance	R <sub>θj-c</sub>	°C/W	0.7

**■Typical Characteristics**

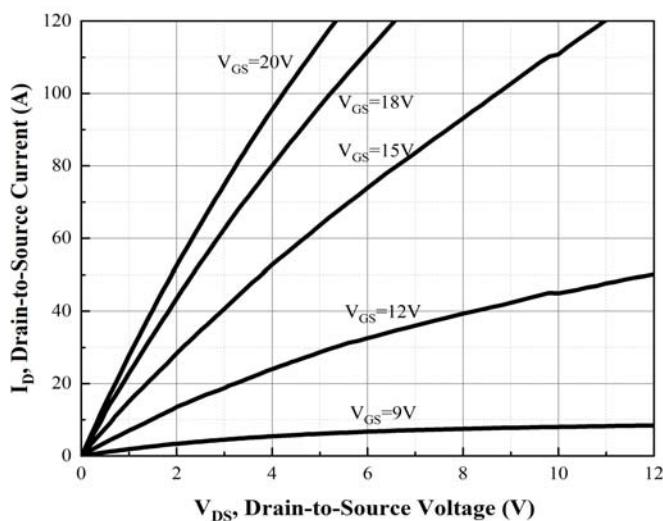


Figure 1. Output Characteristics T<sub>j</sub> = -55°C

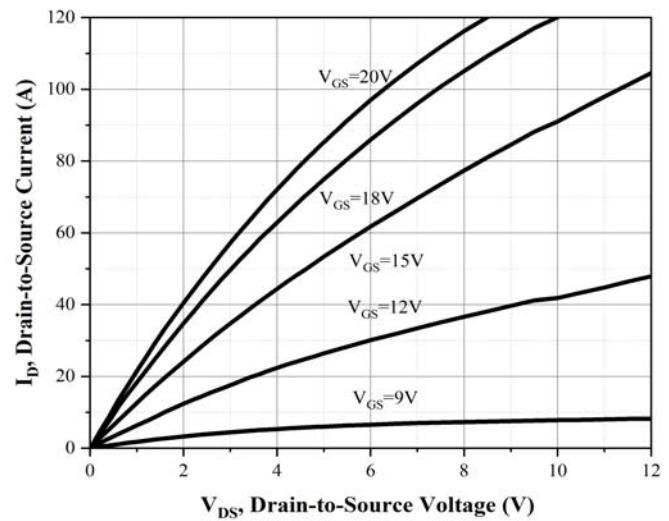


Figure 2. Output Characteristics T<sub>j</sub> = 25°C

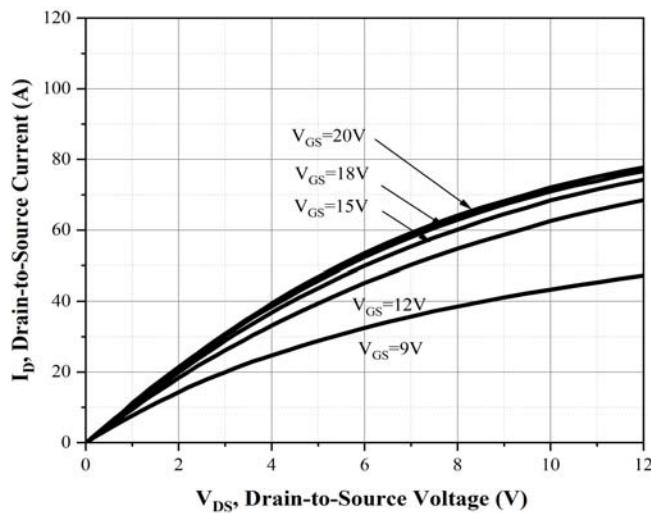


Figure 3. Output Characteristics  $T_j = 175^\circ\text{C}$

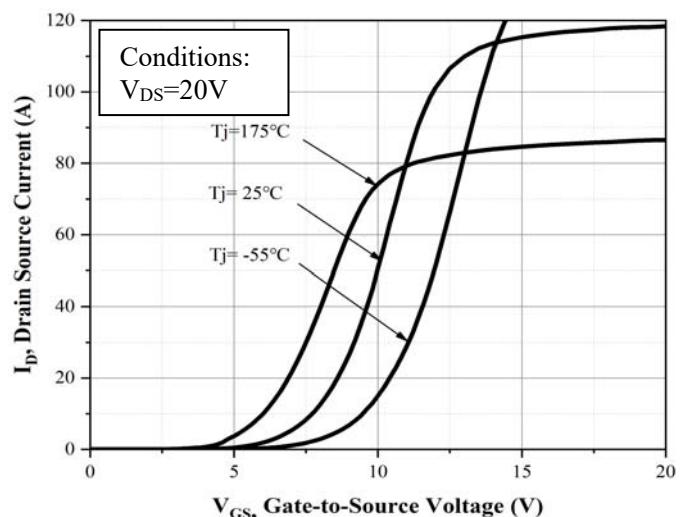


Figure 4. Transfer Characteristics for Various Junction Temperature

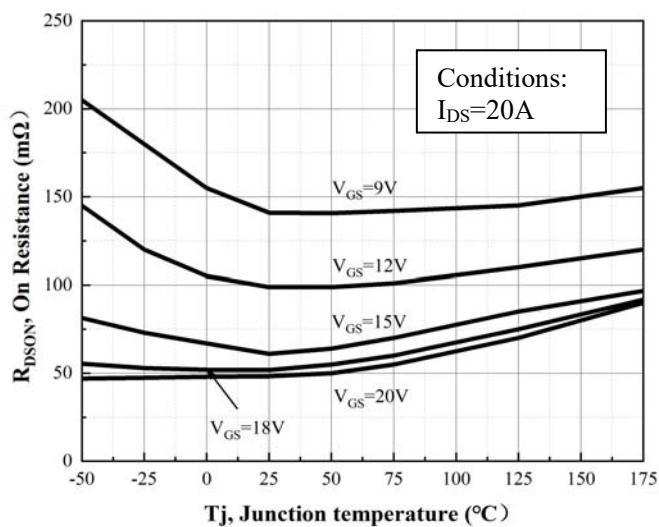


Figure 5. On-resistance vs. Temperature for Various Gate Voltage

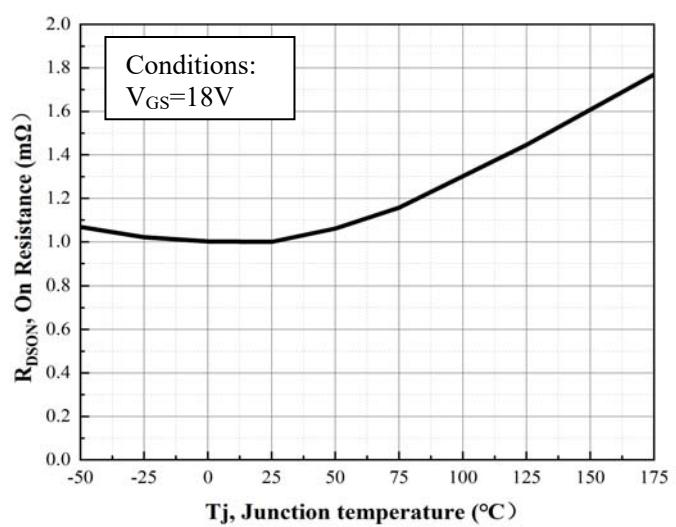


Figure 6. Normalized on-resistance vs. Temperature

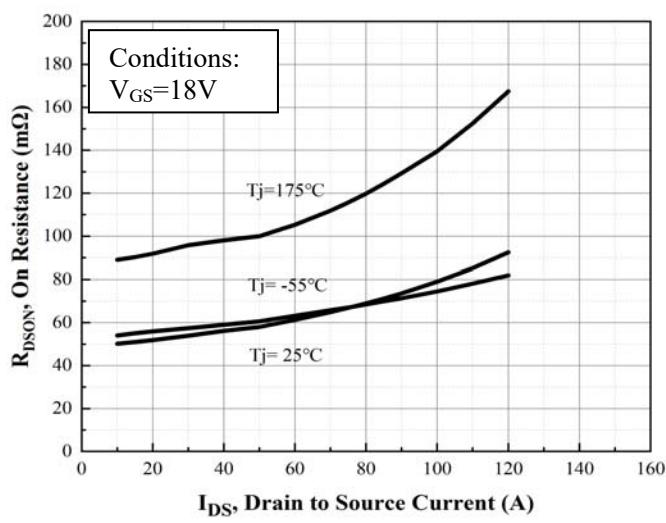


Figure 7. On-resistance vs. Drain Current

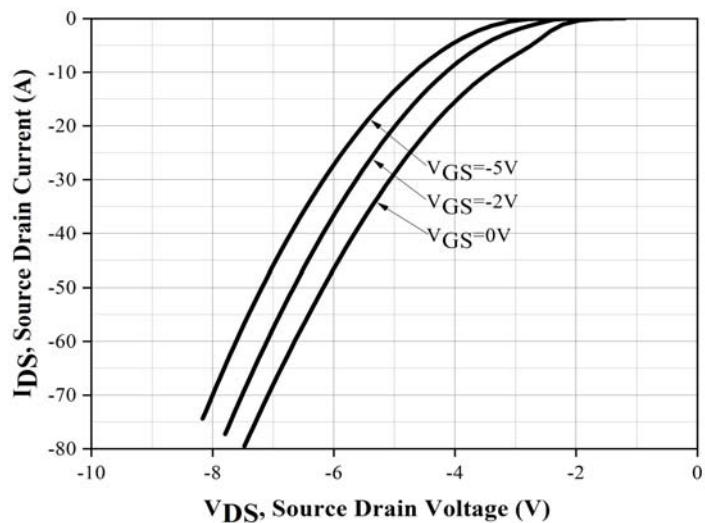


Figure 8. Body Diode Characteristic at  $T_j = 25^\circ\text{C}$

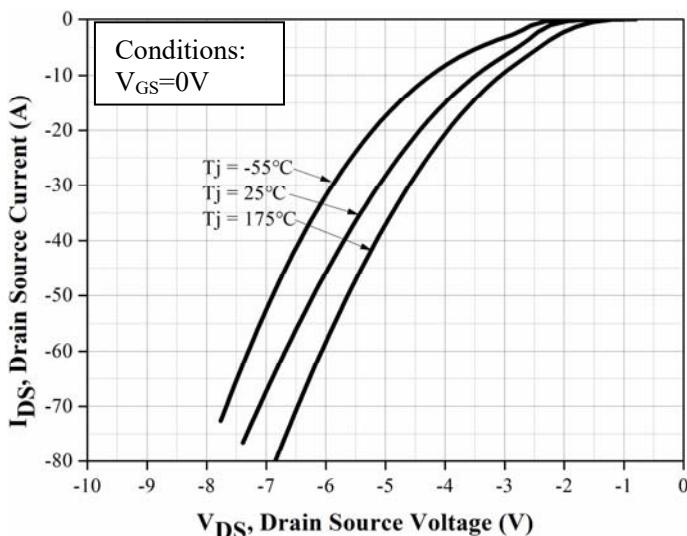


Figure 9. Body Diode Characteristic

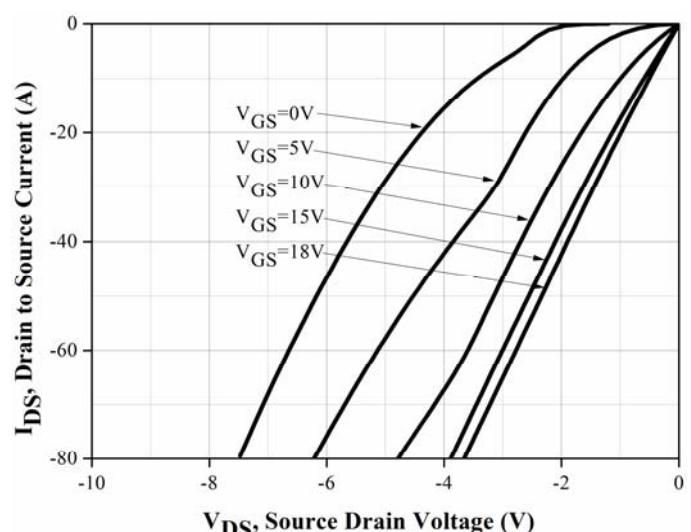


Figure 10. 3<sup>rd</sup> quadrant Characteristic at  $T_j = 25^\circ\text{C}$

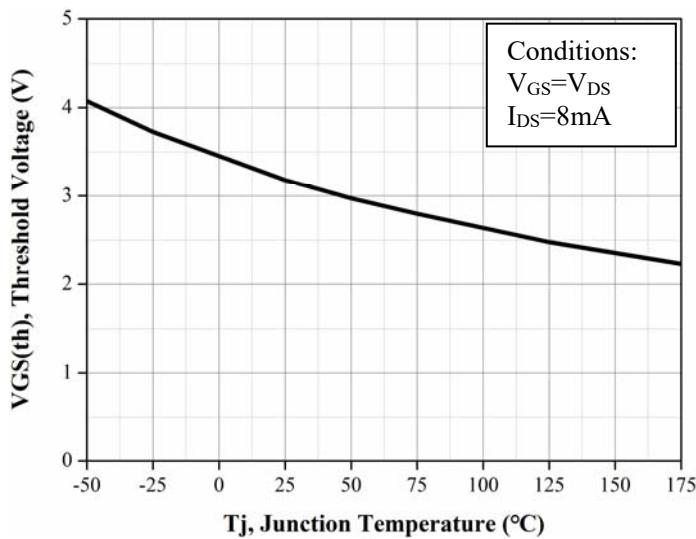


Figure 11. Threshold Voltage vs. Temperature

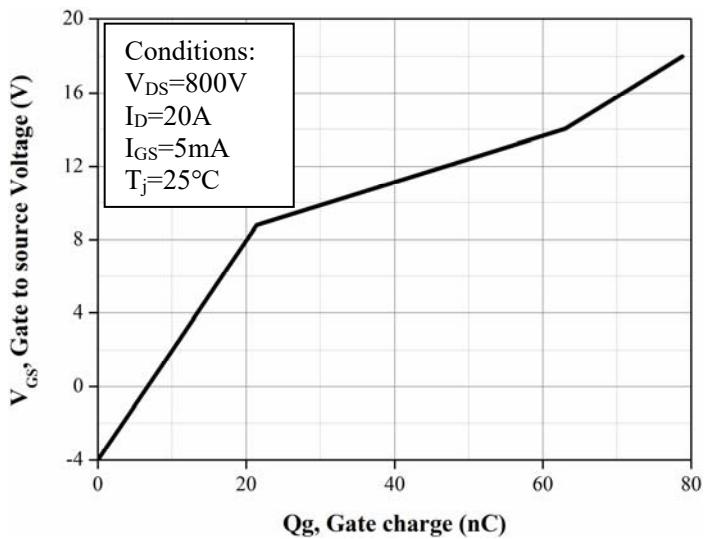


Figure 12. Gate Charge Characteristic

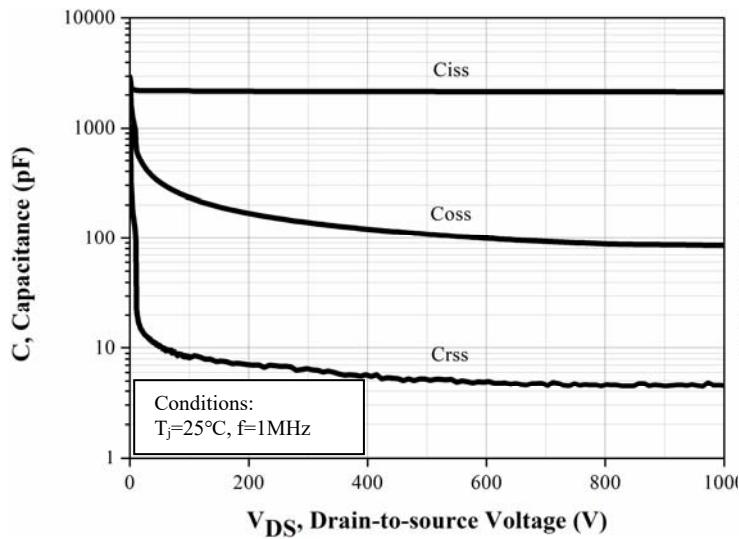


Figure 13. Capacitances vs. Drain Source Voltage (0-1000V)

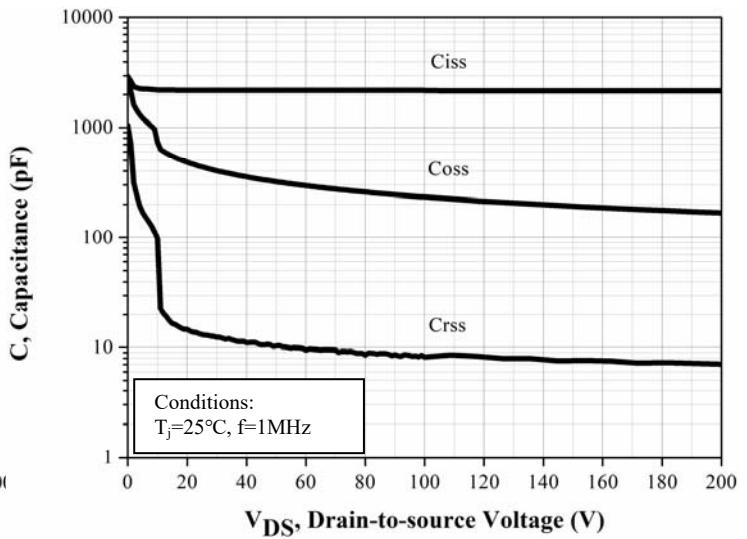


Figure 14. Capacitances vs. Drain Source Voltage (0-200V)

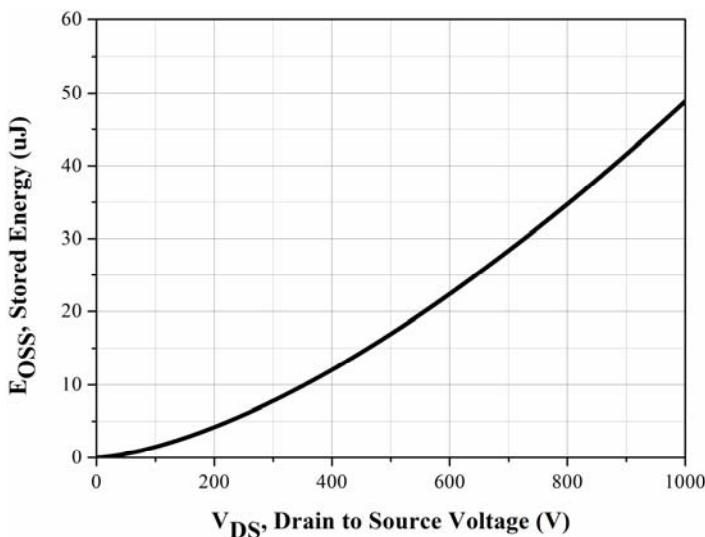


Figure 15. Output Capacitor Stored Energy

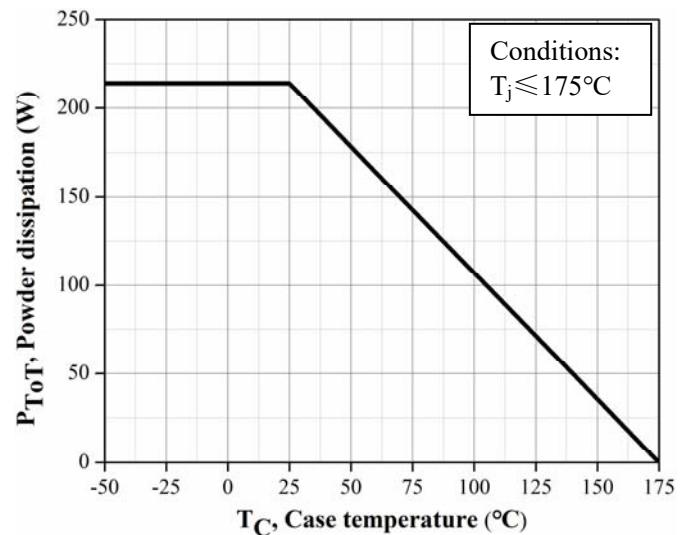


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

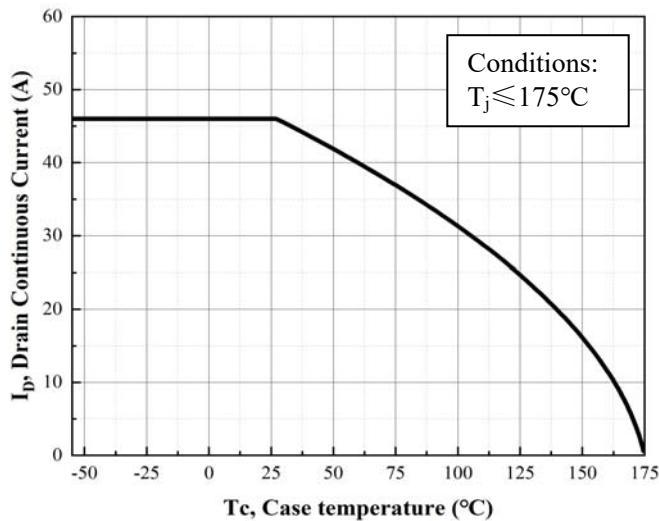


Figure 17. Continuous Drain Current Derating vs. Case Temperature

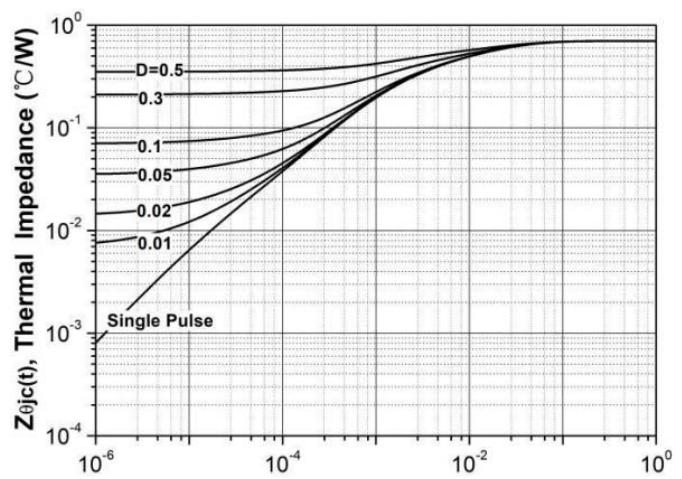


Figure 18 Transient Thermal Impedance (Junction - Case)

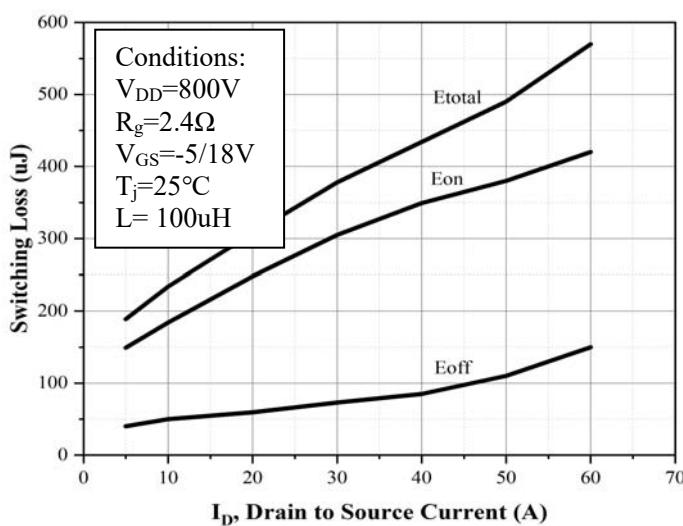


Figure 19. Clamped Inductive Switching Energy vs. Drain Current

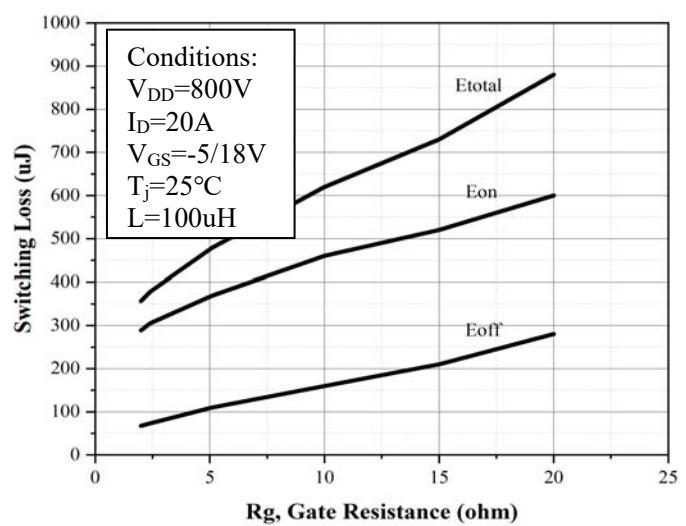


Figure 20. Clamped Inductive Switching Energy vs. Rg

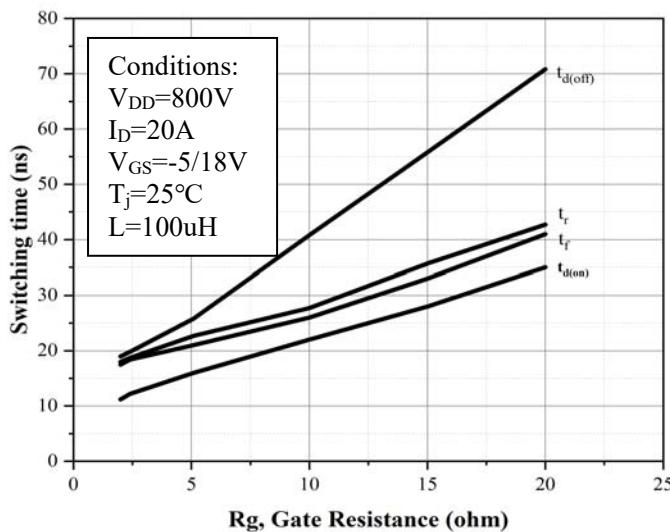


Figure 21. Switching Times vs.  $R_g$

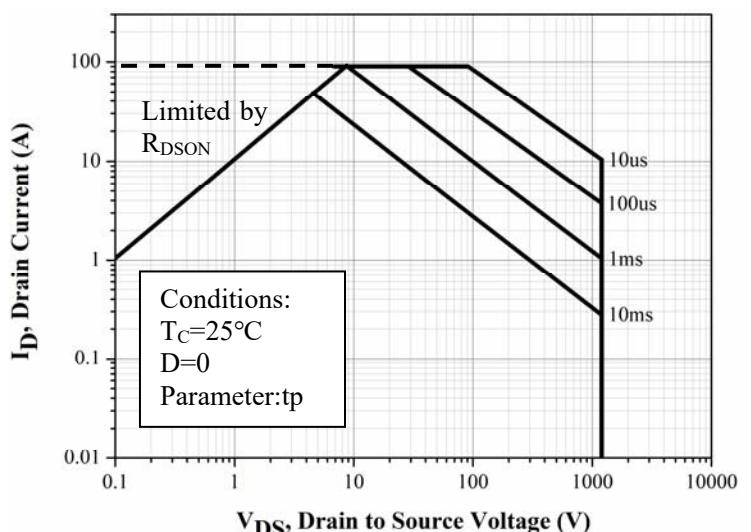


Figure 22. Safe Operating Area

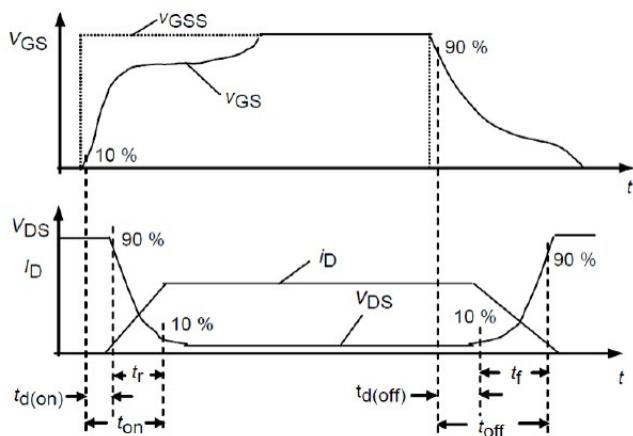


Figure 23. Switching Times Definition

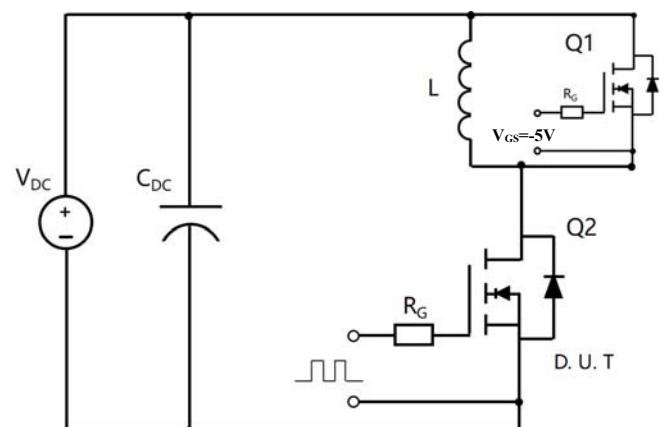
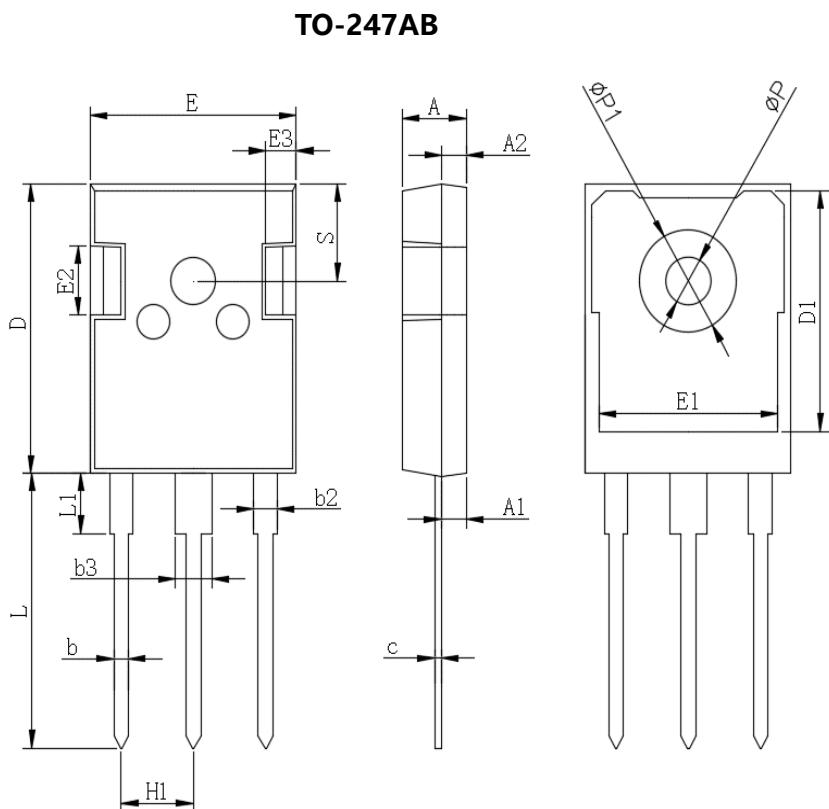


Figure 24. Clamped Inductive Switching Waveform Test Circuit



## ■Outline Dimensions



TO-247AB		
Dim	Min	Max
A	4.80	5.20
A1	2.21	2.61
A2	1.85	2.15
b	1.0	1.4
b2	1.91	2.21
C	0.5	0.7
D	20.70	21.30
D1	16.25	16.85
E	15.50	16.10
E1	13.0	13.6
E2	4.80	5.20
E3	2.30	2.70
L	19.62	20.22
L1	-	4.30
ΦP	3.40	3.80
ΦP1	-	7.30
S	6.15TYP	
H1	5.44TYP	
b3	2.80	3.20



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