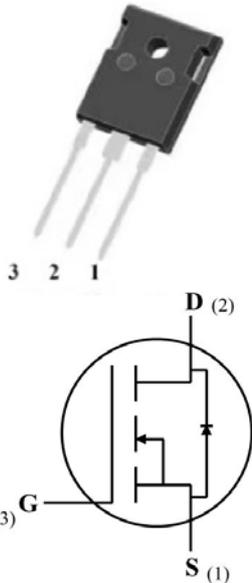


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	1200V
I_D (25°C)	80A
$R_{DS(on)}$	24mΩ



Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TO-247AB
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■ Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D212025NCTYG3		
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	1200	$V_{GS}=0V, I_D=100\mu A$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-8/+22	Absolute maximum values	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-5/+18	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	I_D	A	80	$V_{GS}=18V, T_c=25^\circ\text{C}$	Fig.17
Continuous drain current @ $T_c=100^\circ\text{C}$			55	$V_{GS}=18V, T_c=100^\circ\text{C}$	
Pulsed drain current	$I_{D(pulsed)}$	A	178	Pulse width t_p limited by $T_{j,max}$	Fig.22
Avalanche energy, Single pulse	E_{AS}	J	1.1	$V_{DD}=75V, L=30mH$	
Power Dissipation	P_{TOT}	W	365	$T_c=25^\circ\text{C}, T_j=175^\circ\text{C}$	Fig.16
Power Dissipation			182	$T_c=100^\circ\text{C}, T_j=175^\circ\text{C}$	
Operating junction and Storage temperature range	T_j, T_{stg}	$^\circ\text{C}$	-55 to +175		
Soldering temperature	T_L	$^\circ\text{C}$	260	1.6mm (0.063") from case for 10s	
Mounting torque	T_M	Nm	0.6	M3 screw Maximum of mounting process: 3	



YJD212025NCTYG3

■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	V _{GS(th)}	V	2.0	2.7	4.0	V _{DS} =V _{GS} , I _D = 15mA	Fig.4, 11
				2.0		V _{DS} =V _{GS} , I _D = 15mA, T _j =175°C	
Drain source breakdown voltage	V _{(BR)DSS}	V	1200			V _{GS} =0V, I _D =100uA	
Drain source leakage current	I _{DSS}	uA		10	100	V _{DS} =1200V, V _{GS} = 0V	
Gate source leakage current	I _{GSS}	nA		10	100	V _{GS} = 18V, V _{DS} =0V	
Current drain source on-state resistance	R _{DS ON}	mΩ		24	30	V _{GS} =18V, I _D =40A	Fig.5, 6, 7
				43		V _{GS} =18V, I _D =40A, T _j =175°C	
Transconductance	g _r	S		28		V _{DS} =20V, I _D =40A	Fig.4
				26		V _{DS} =20V, I _D =40A, T _j =175°C	

■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C _{iss}	pF		4710		V _{DS} =800V, V _{GS} =0V, T _j =25°C, f=1MHz, V _{AC} = 25mV	Fig.13, 14
Output capacitance	C _{oss}			160			
Reverse capacitance	C _{rss}			6.8			
Coss stored energy	E _{oss}	uJ		84			Fig.15
Gate source charge	Q _{gs}	nC		40		V _{DS} =800V, V _{GS} =-5/18V, I _D =40A	Fig.12
Gate drain charge	Q _{gd}			61			
Gate charge	Q _g			152			
Internal gate resistance	R _g	Ω		2		f=1MHz	

■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on switching energy	E _{on}	uJ		865		V _{DD} =800V, V _{GS} =-5/+18V, I _D =40A, R _g =2.4Ω, L=100uH	Fig.19, 20
Turn off switching energy	E _{off}			323			
Turn on delay time	t _{d(on)}	ns		18		V _{DD} =800V, V _{GS} =-5/+18V, I _D =40A, R _g =2.4Ω, L=100uH	Fig.21
Rise time	t _r			43			
Turn off delay time	t _{d(off)}	ns		29			
Fall time	t _f			15			



YJD212025NCTYG3

■Body diode characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V _{SD}	V		4.8		V _{GS} =-5V, I _{SD} =20A	Fig.8
				3		V _{GS} =0V, I _{SD} =20A, T _j =175°C	Fig.9
Continuous diode forward current	I _S	A		57		V _{GS} =-5V, T _c =25°C	
Reverse recovery time	t _{rr}	nS		48		V _R =800V, V _{GS} =-5V, I _D =40A, di/dt=2500A/uS	
Reverse recovery charge	Q _{rr}	nC		323			
Peak reverse recovery current	I _{rrm}	A		11			

■Thermal Characteristics (T_a=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Typ.
Thermal resistance	R _{θJ-C}	°C/W	0.41

■Typical Characteristics

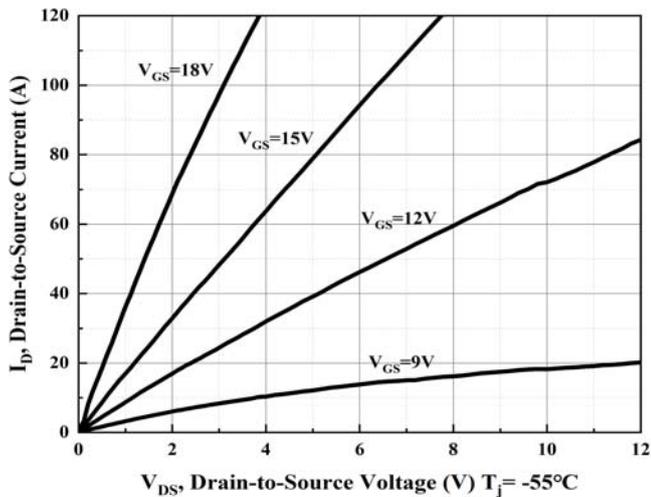


Figure 1. Output Characteristics T_j = -55°C

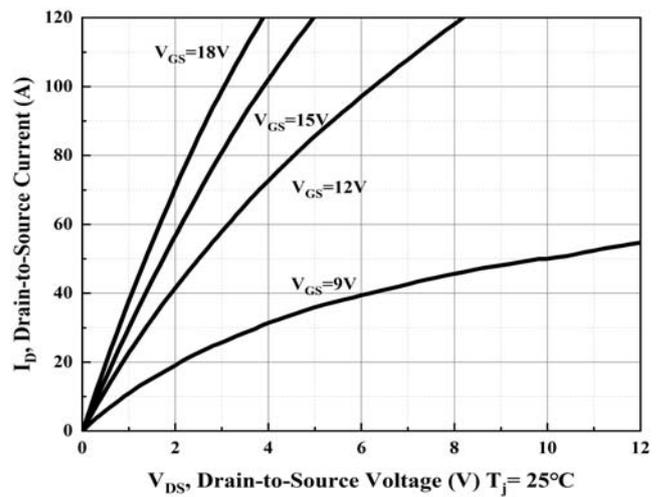


Figure2. Output Characteristics T_j = 25°C



YJD212025NCTYG3

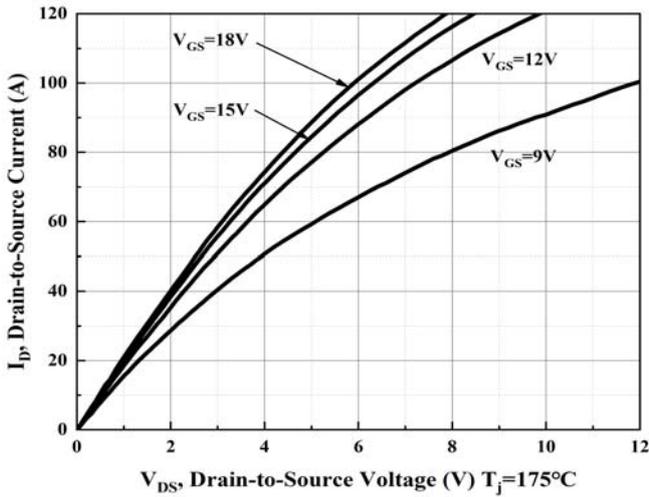


Figure 3. Output Characteristics $T_j = 175^\circ\text{C}$

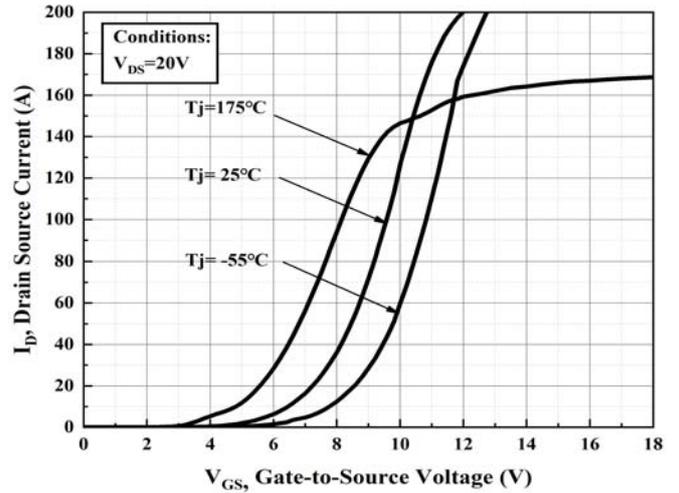


Figure 4. Transfer Characteristics for Various Junction Temperature

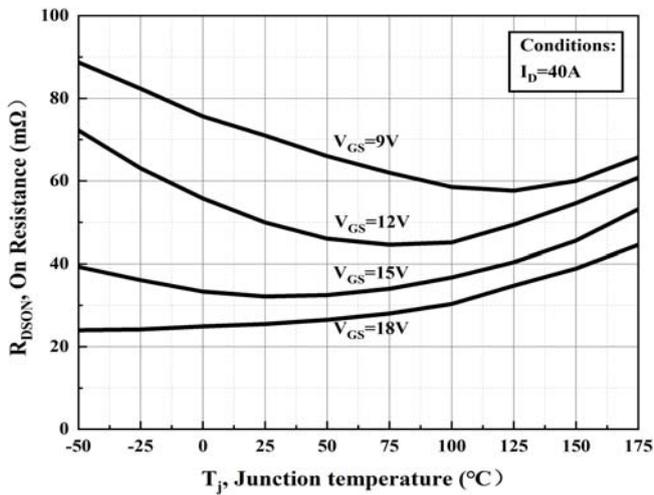


Figure 5. On-resistance vs. Temperature for Various Gate Voltage

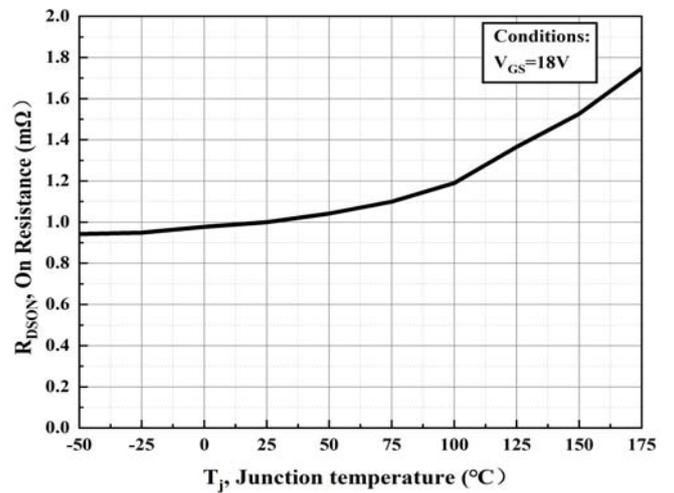


Figure 6. Normalized on-resistance vs. Temperature

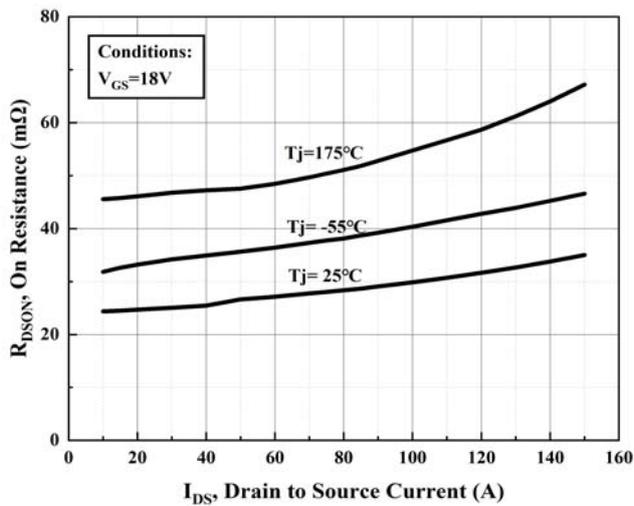


Figure 7. On-resistance vs. Drain Current

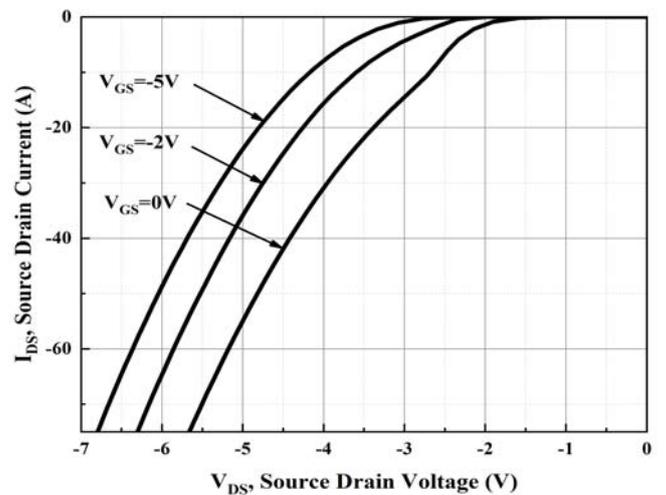


Figure 8. Body Diode Characteristic at $T_j = 25^\circ\text{C}$



YJD212025NCTYG3

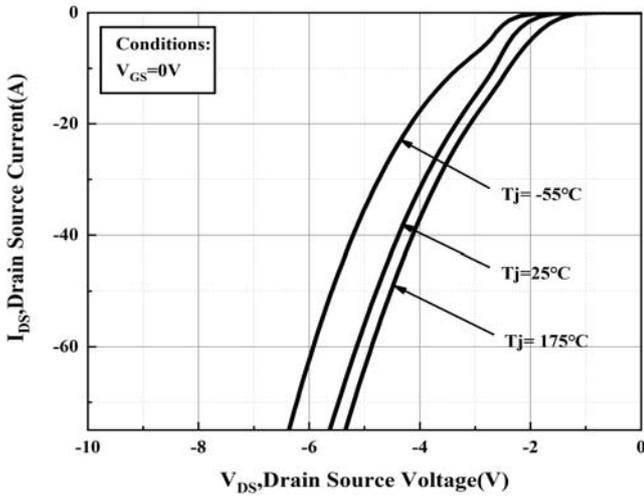


Figure 9. Body Diode Characteristic

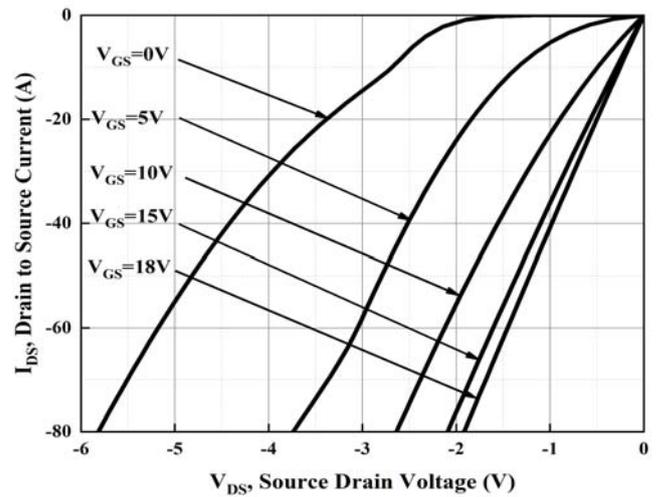


Figure 10. 3rd quadrant Characteristic at Tj= 25 °C

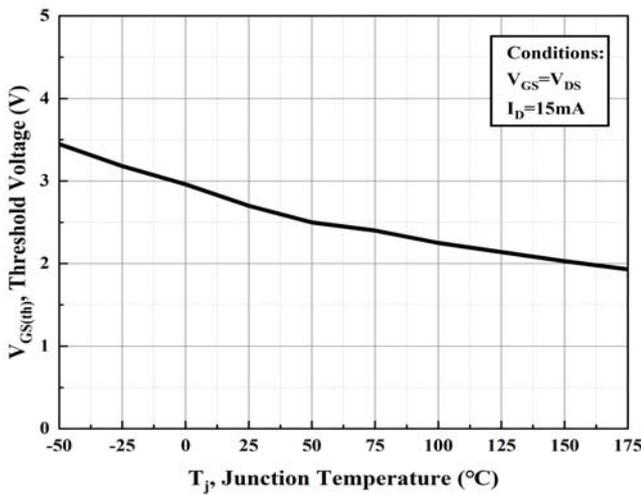


Figure 11. Threshold Voltage vs. Temperature

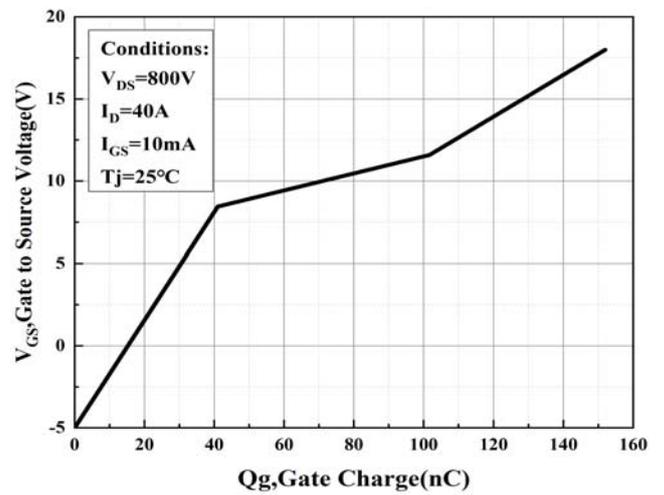


Figure 12. Gate Charge Characteristic

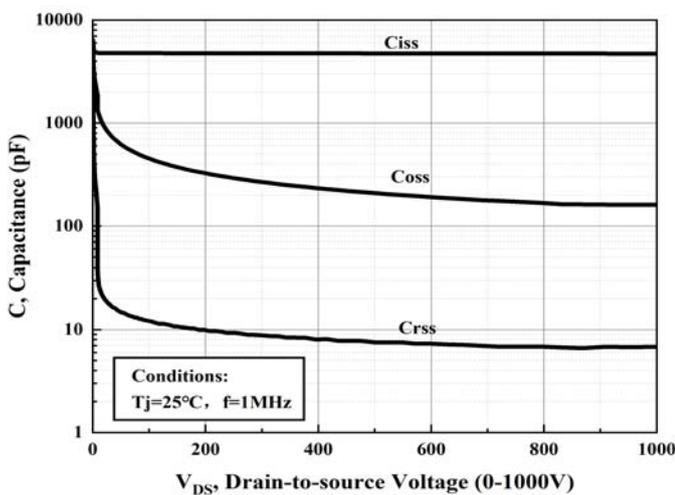


Figure 13. Capacitances vs. Drain Source Voltage (0-1000V)

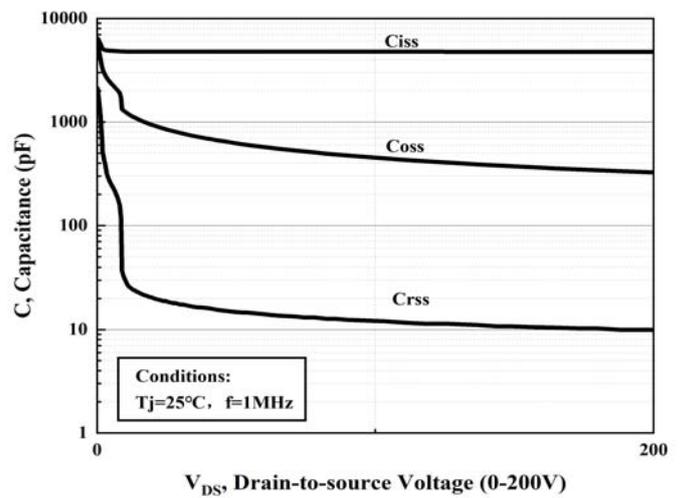


Figure 14. Capacitances vs. Drain Source Voltage (0-200V)



YJD212025NCTYG3

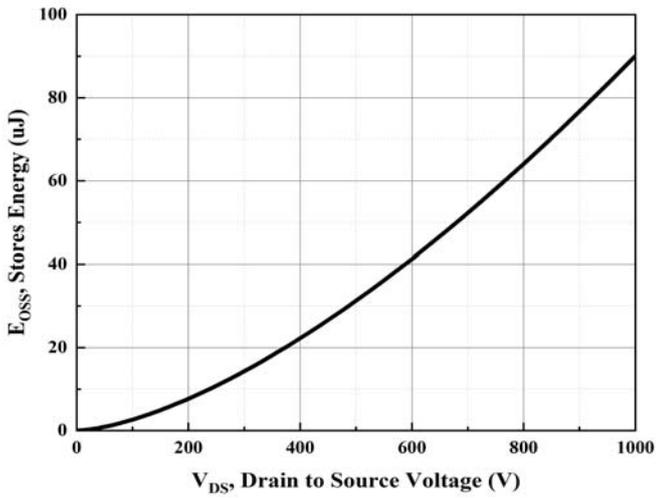


Figure 15. Output Capacitor Stored Energy

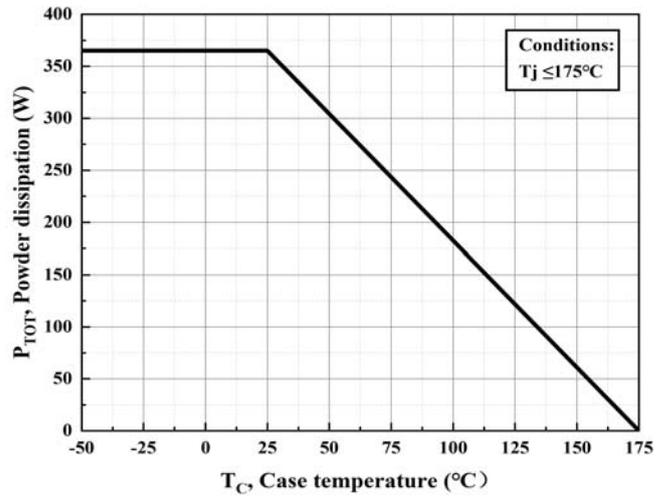


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

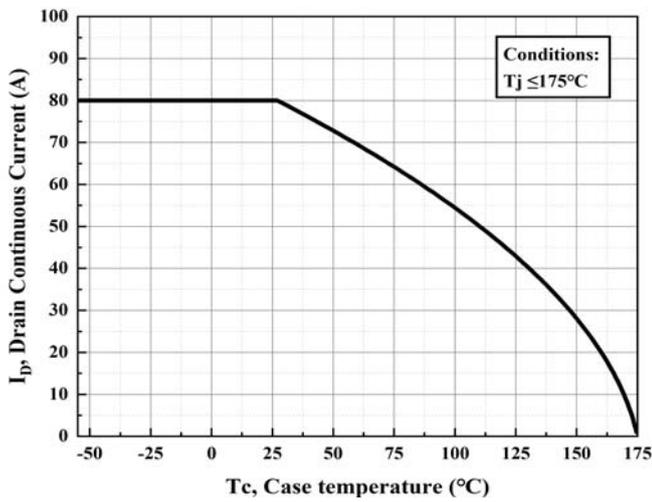


Figure 17. Continuous Drain Current Derating vs. Case Temperature

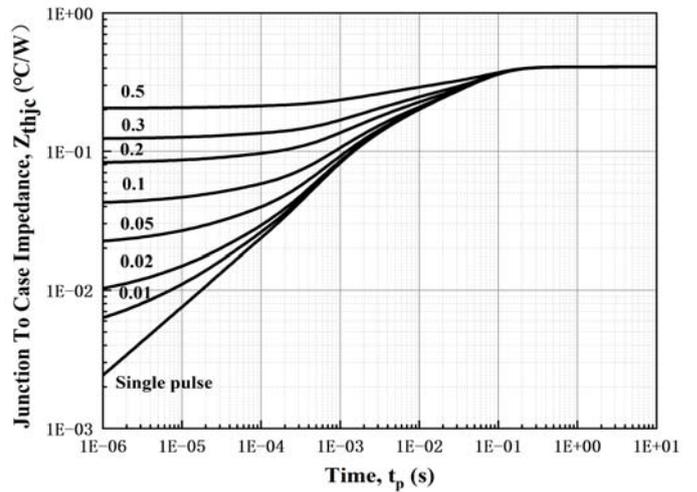


Figure 18. Transient Thermal Impedance (Junction - Case)

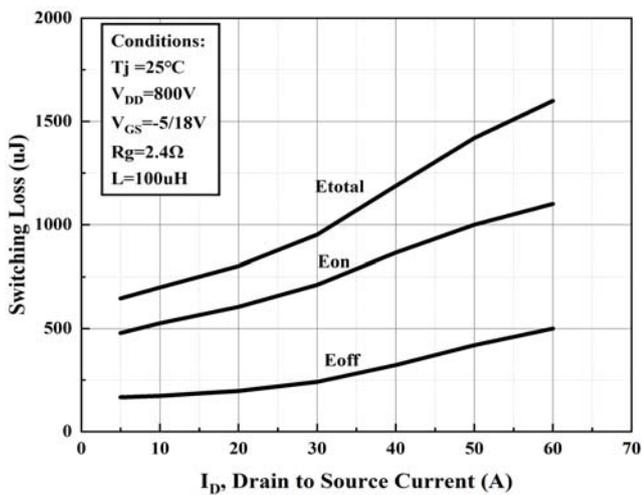


Figure 19. Clamped Inductive Switching Energy vs. Drain Current

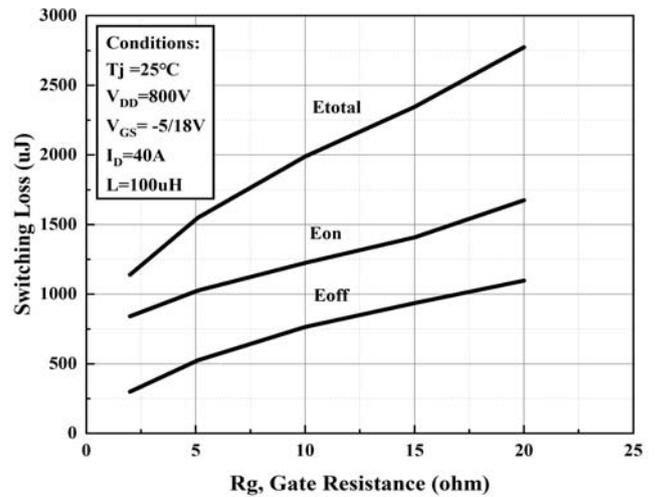


Figure 20. Clamped Inductive Switching Energy vs. R_g

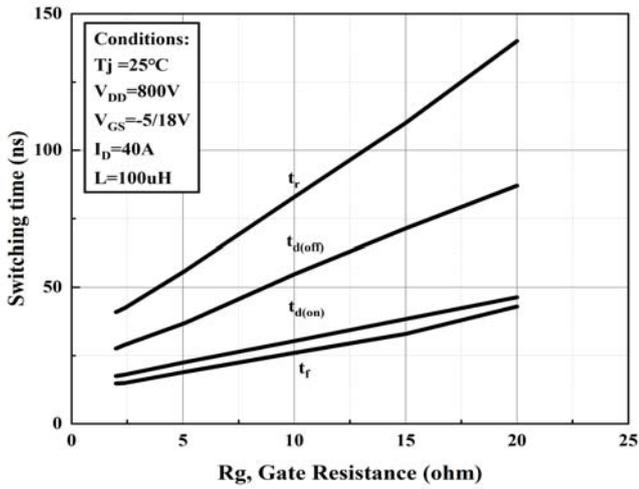


Figure 21. Switching Times vs. Rg

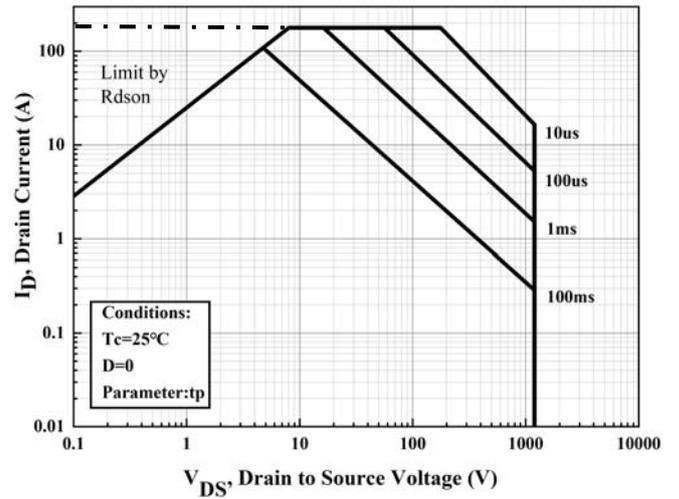


Figure 22. Safe Operating Area

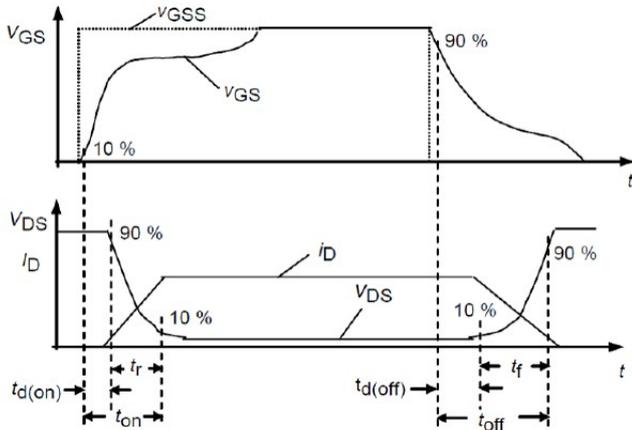


Figure 23. Switching Times Definition

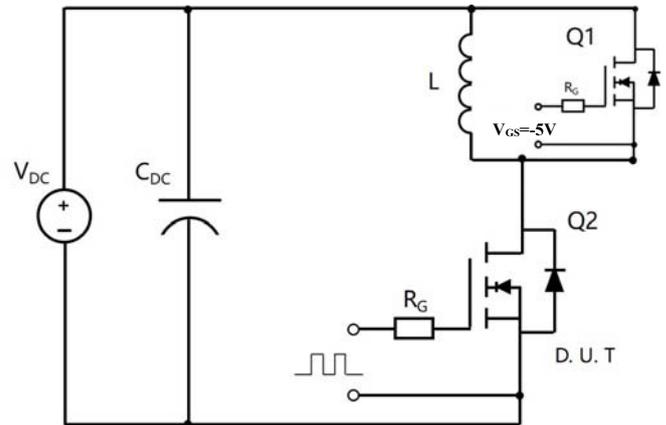
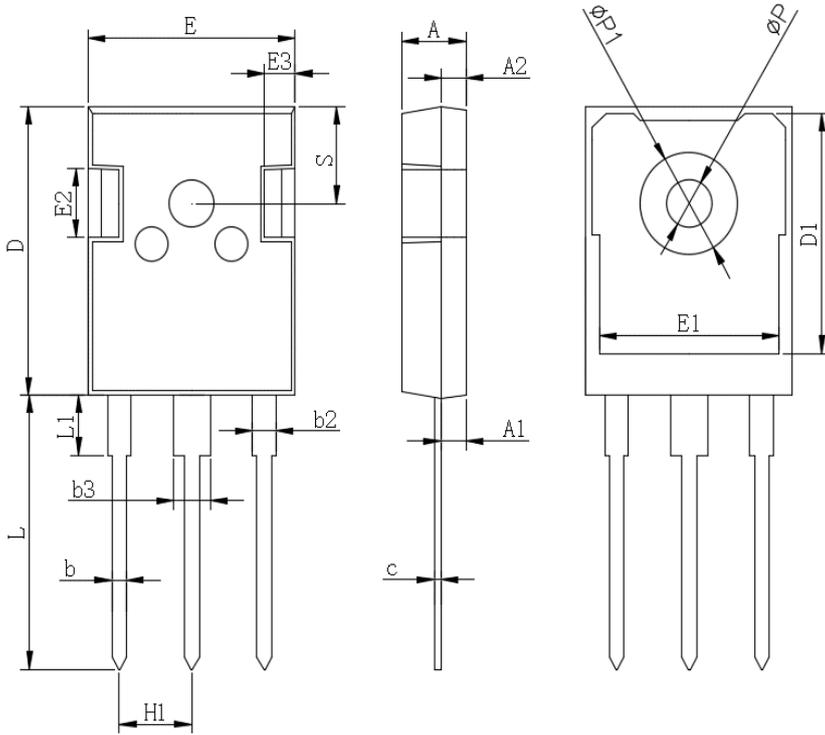


Figure 24. Clamped Inductive Switching Waveform Test Circuit

■Outline Dimensions

TO-247AB



TO-247AB		
Dim	Min	Max
A	4.80	5.20
A1	2.21	2.61
A2	1.85	2.15
b	1.0	1.4
b2	1.91	2.21
C	0.5	0.7
D	20.70	21.30
D1	16.25	16.85
E	15.50	16.10
E1	13.0	13.6
E2	4.80	5.20
E3	2.30	2.70
L	19.62	20.22
L1	-	4.30
ϕP	3.40	3.80
$\phi P1$	-	7.30
S	6.15TYP	
H1	5.44TYP	
b3	2.80	3.20



YJD212025NCTYG3

Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, function or design or otherwise.

The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with equipment or devices which require high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use of sale.

This publication supersedes & replaces all information previously supplied. For additional information, please visit our website [http:// www.21yangjie.com](http://www.21yangjie.com) , or consult your nearest Yangjie's sales office for further assistance.